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on September 20, 2001

TOWNSEND and TOWNSEND and CREW LLP

By

Andrea S. Beck

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Takashi Hiroi, et al.

Serial No.: 09/802,693

Filed: March 8, 2001

For: **A Circuit Pattern Inspection  
Method and Apparatus**

Examiner: Not Yet Assigned

Art Unit: 2173

LETTER TO OFFICIAL DRAFTSPERSON

Official Draftsperson  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Applicant hereby, submits 21 sheets of formal drawings to be made of record in the above-identified case.

Respectfully submitted,

Chun-Pok Leung  
Reg. No. 41,405

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PA 154582 v1  
PA 3171740 v1

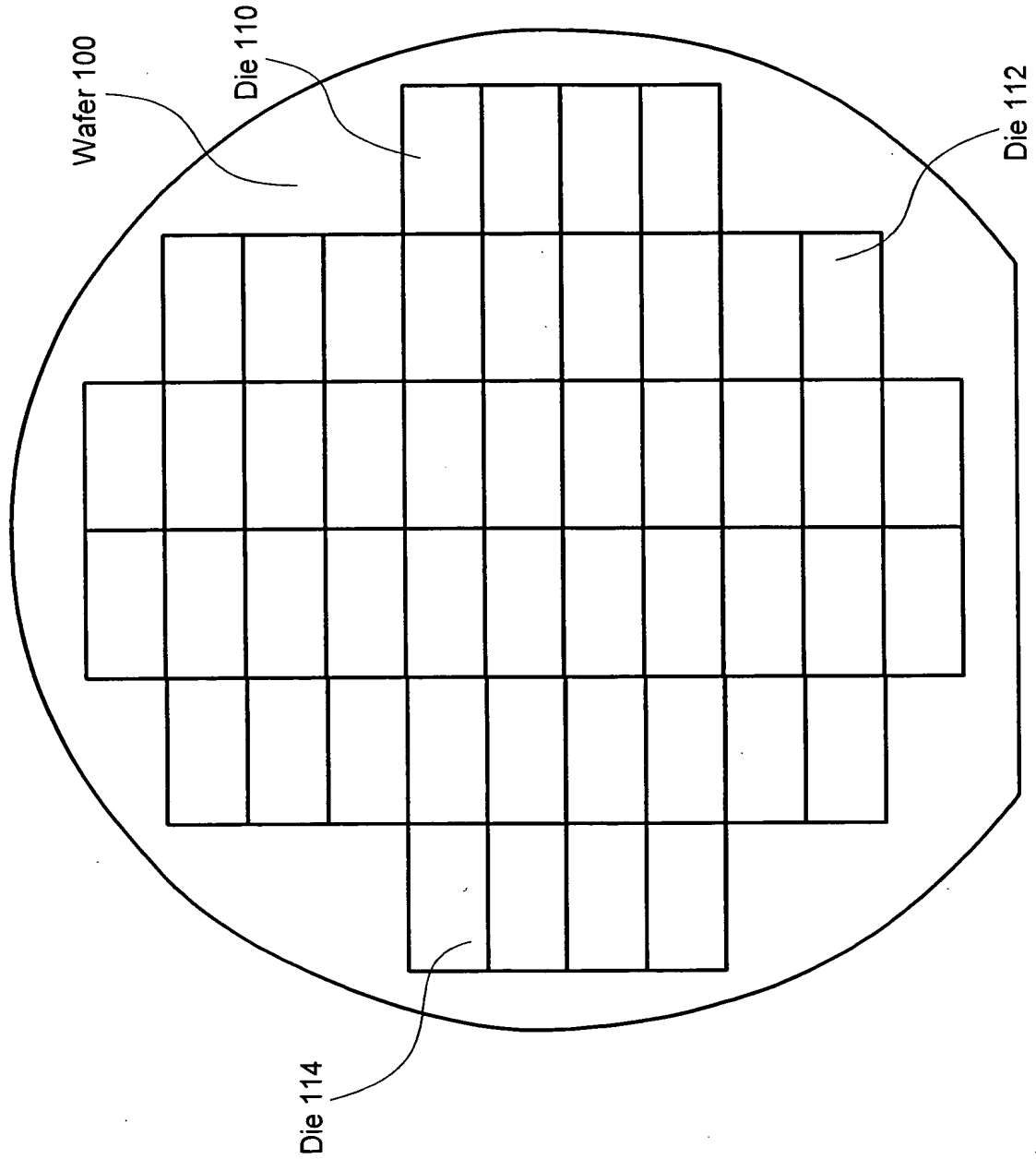
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Attorney Docket No.: 16869P-  
017810US  
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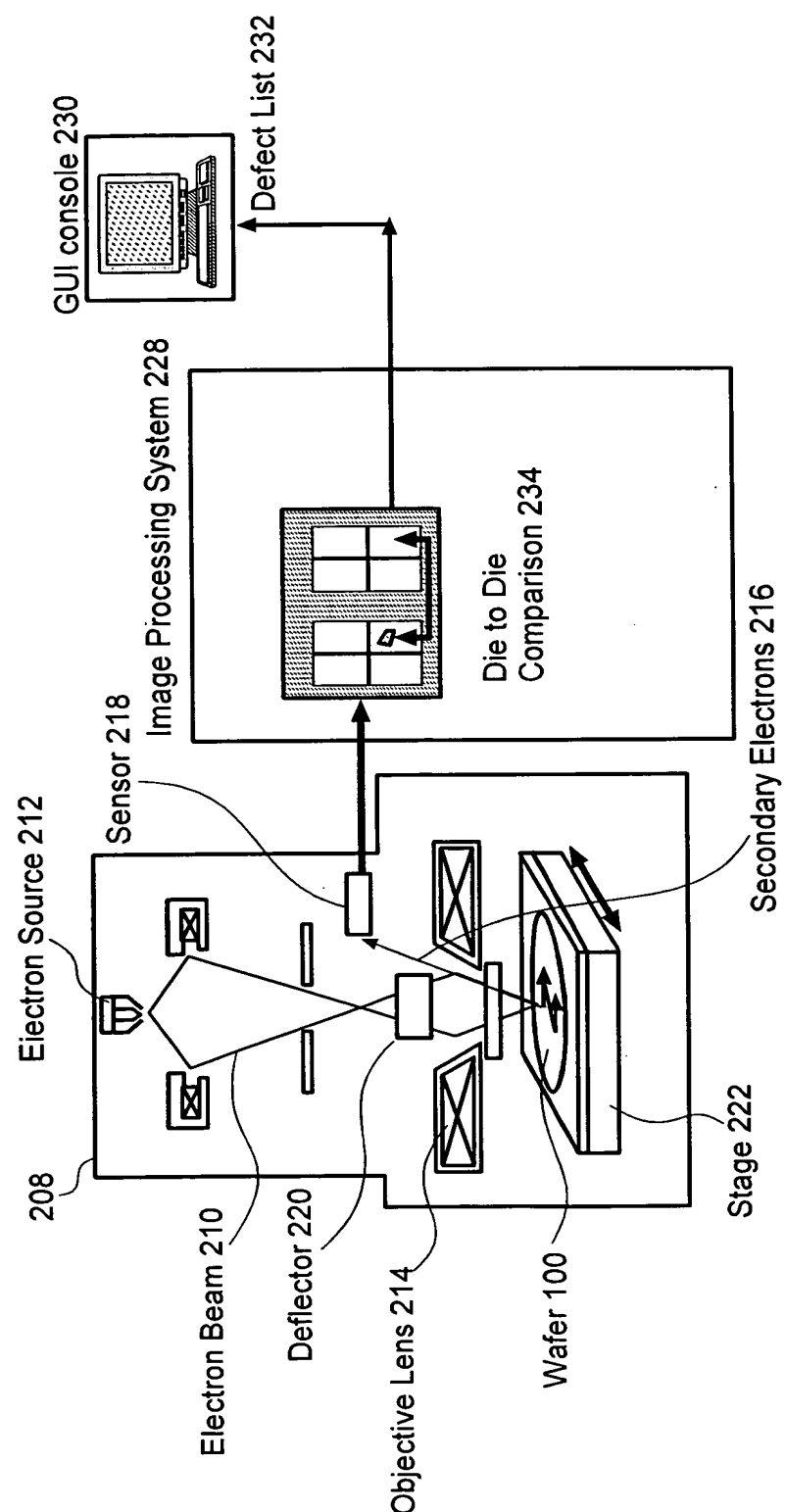
APPROVED	O.G. FIG.	
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Chun-Pok Leung, Reg. No. 41,405, (650) 326-2400  
Titled: A Circuit Pattern Inspection Method and Apparatus  
App. No. 09/802,693 Filed: March 8, 2001  
Dckt. No. 16869P-017810US  
Sheet 1 of 21

**FIG.1**  
(PRIOR ART)



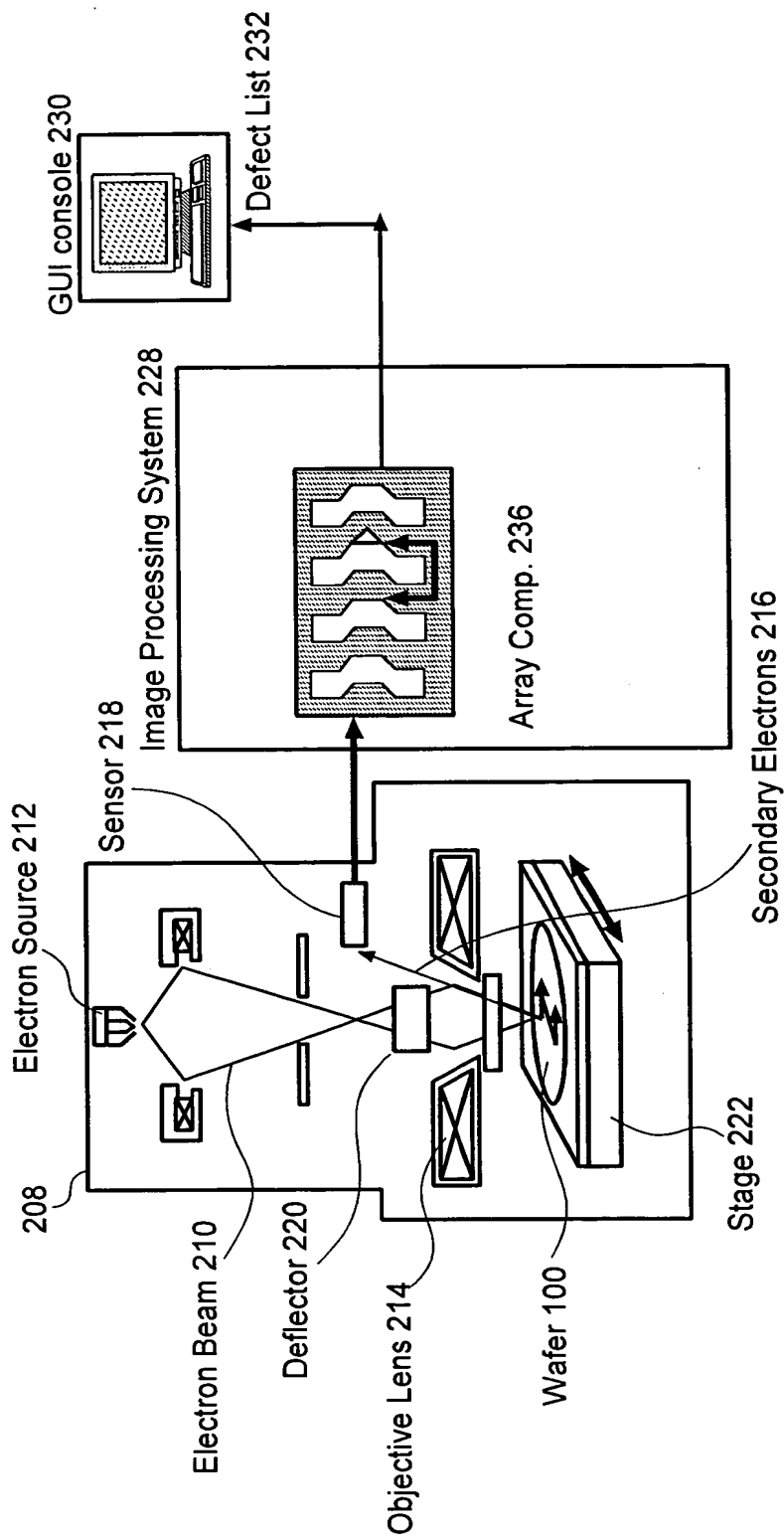
**FIG. 2**  
 (PRIOR ART)



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**FIG. 3**

(PRIOR ART)



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**FIG.4**  
 (PRIOR ART)

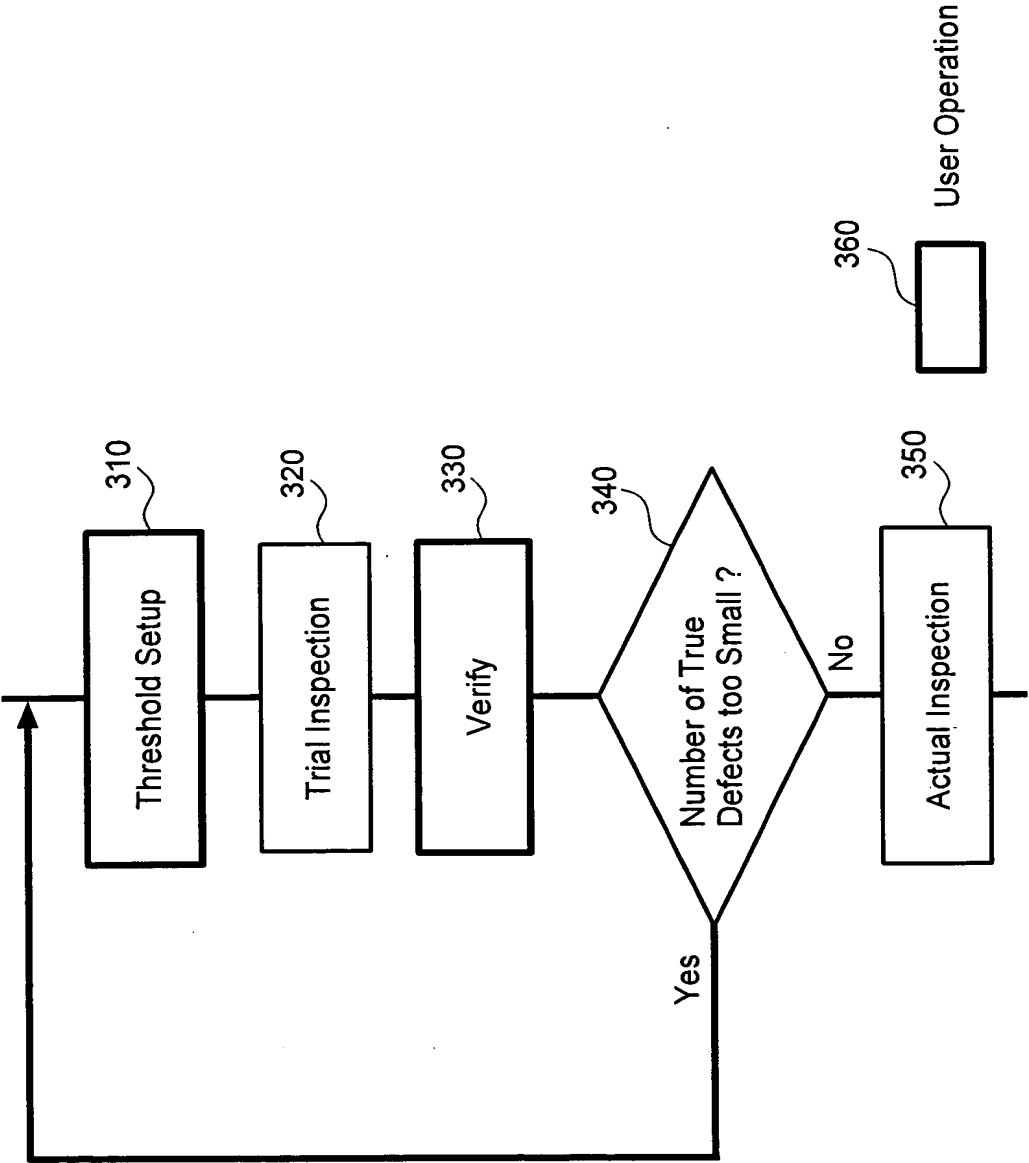
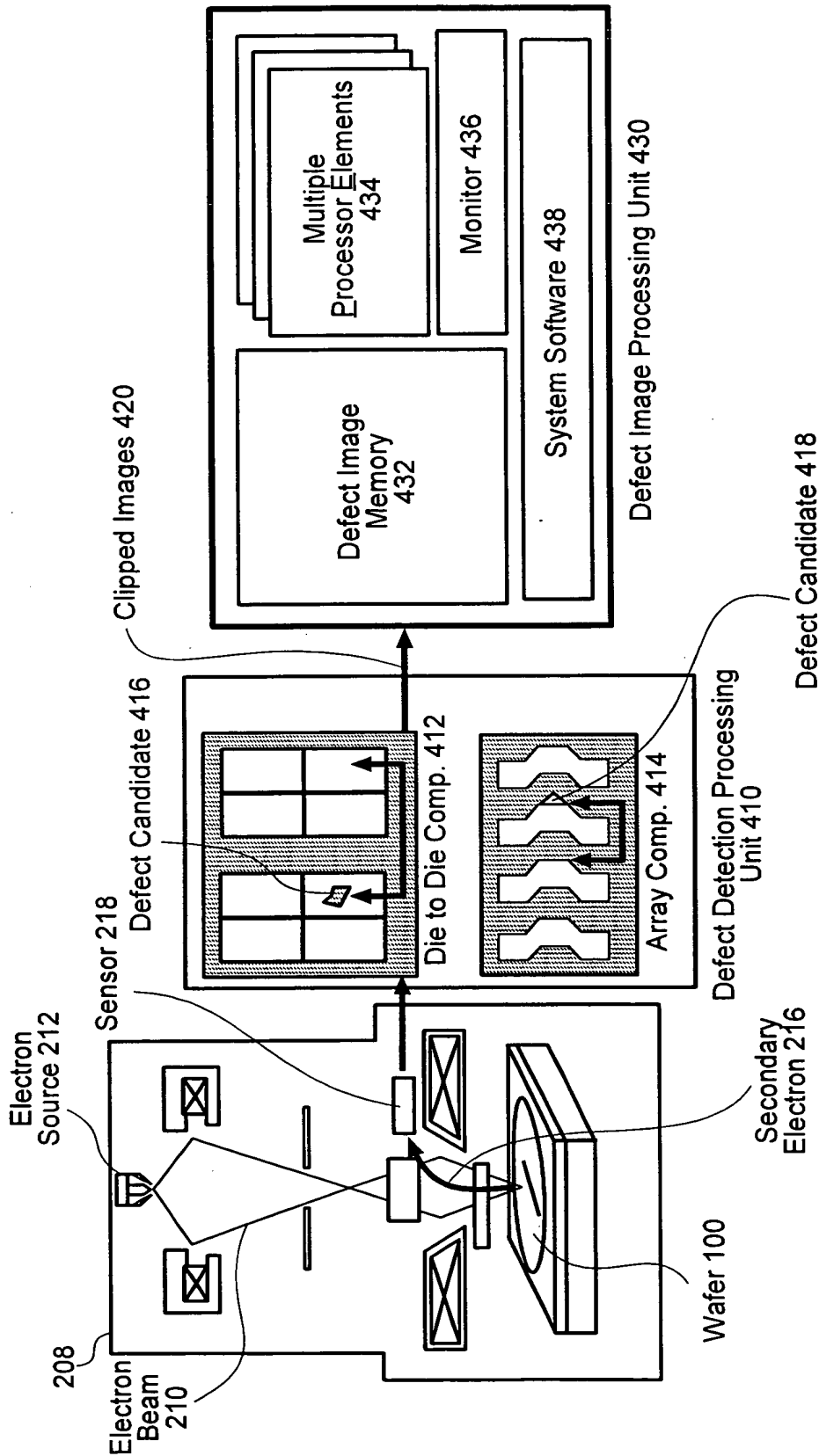
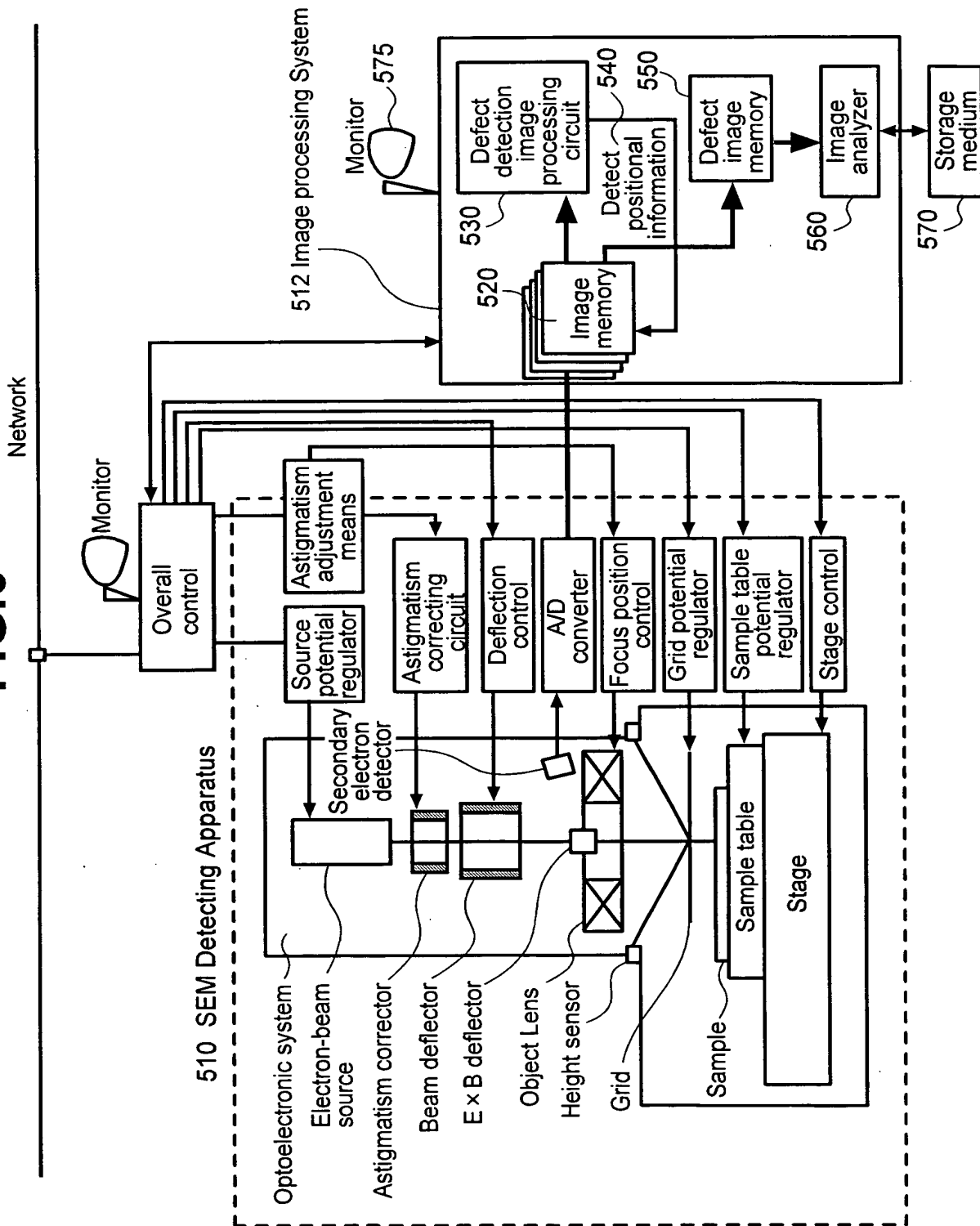


FIG. 5

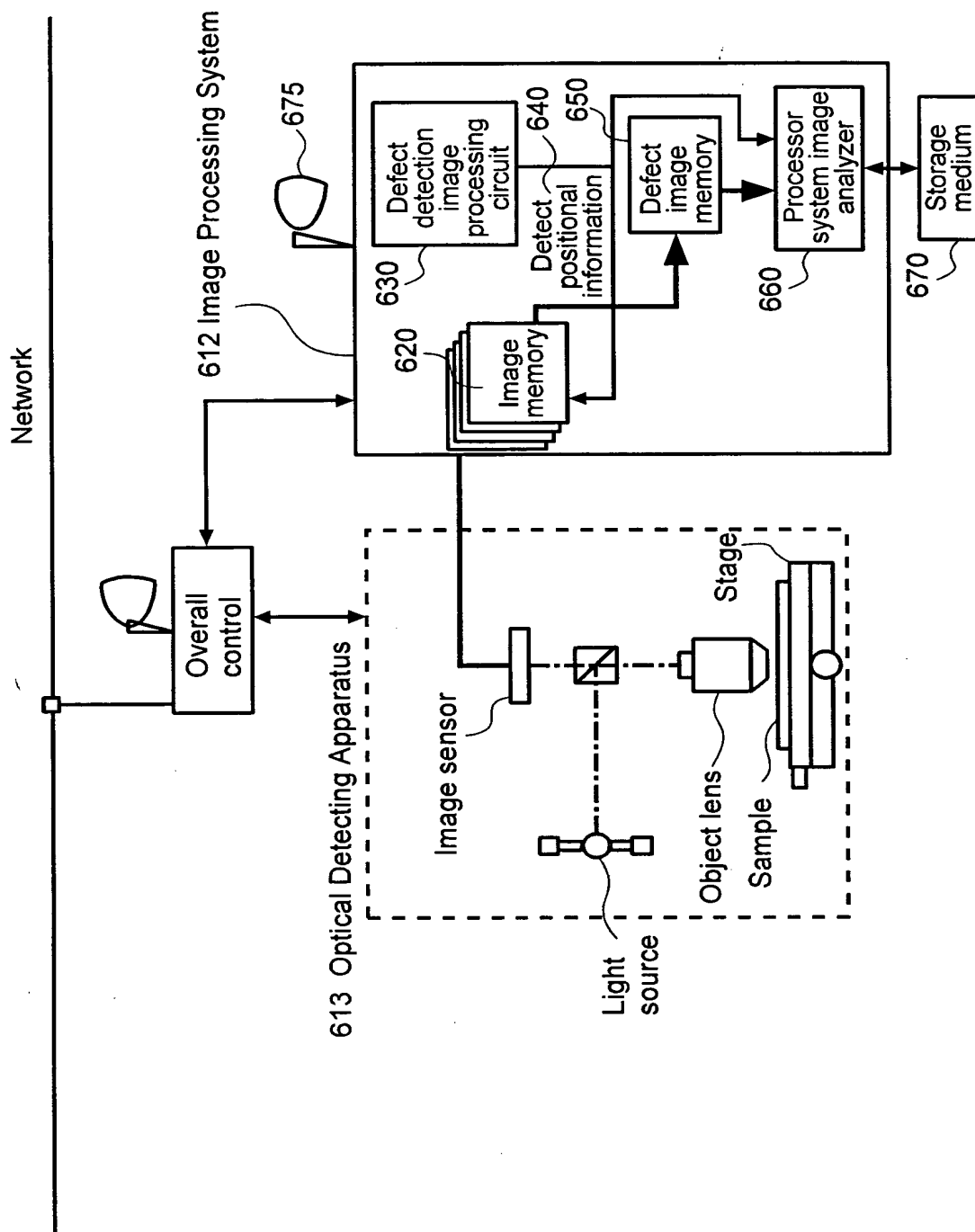


**FIG. 6**



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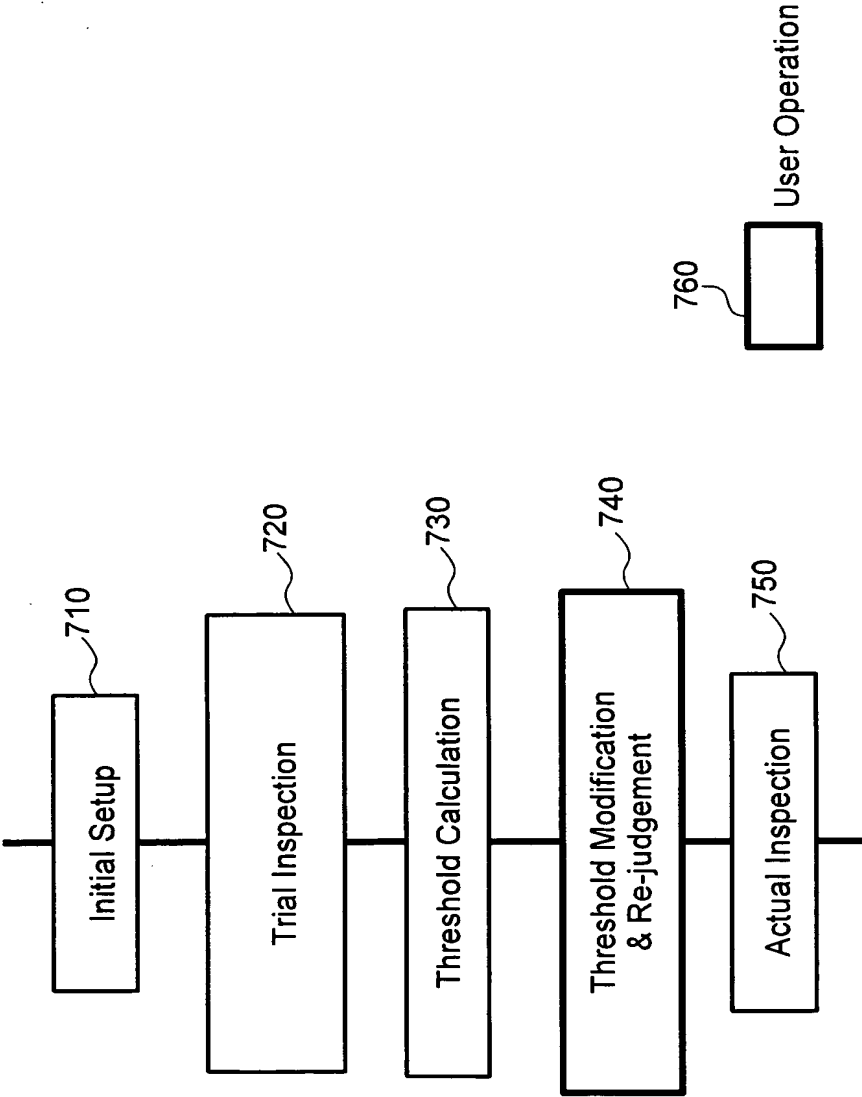
**FIG.7**





APPROVED	C.G. FIG.	
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FIG.8



**FIG. 9**

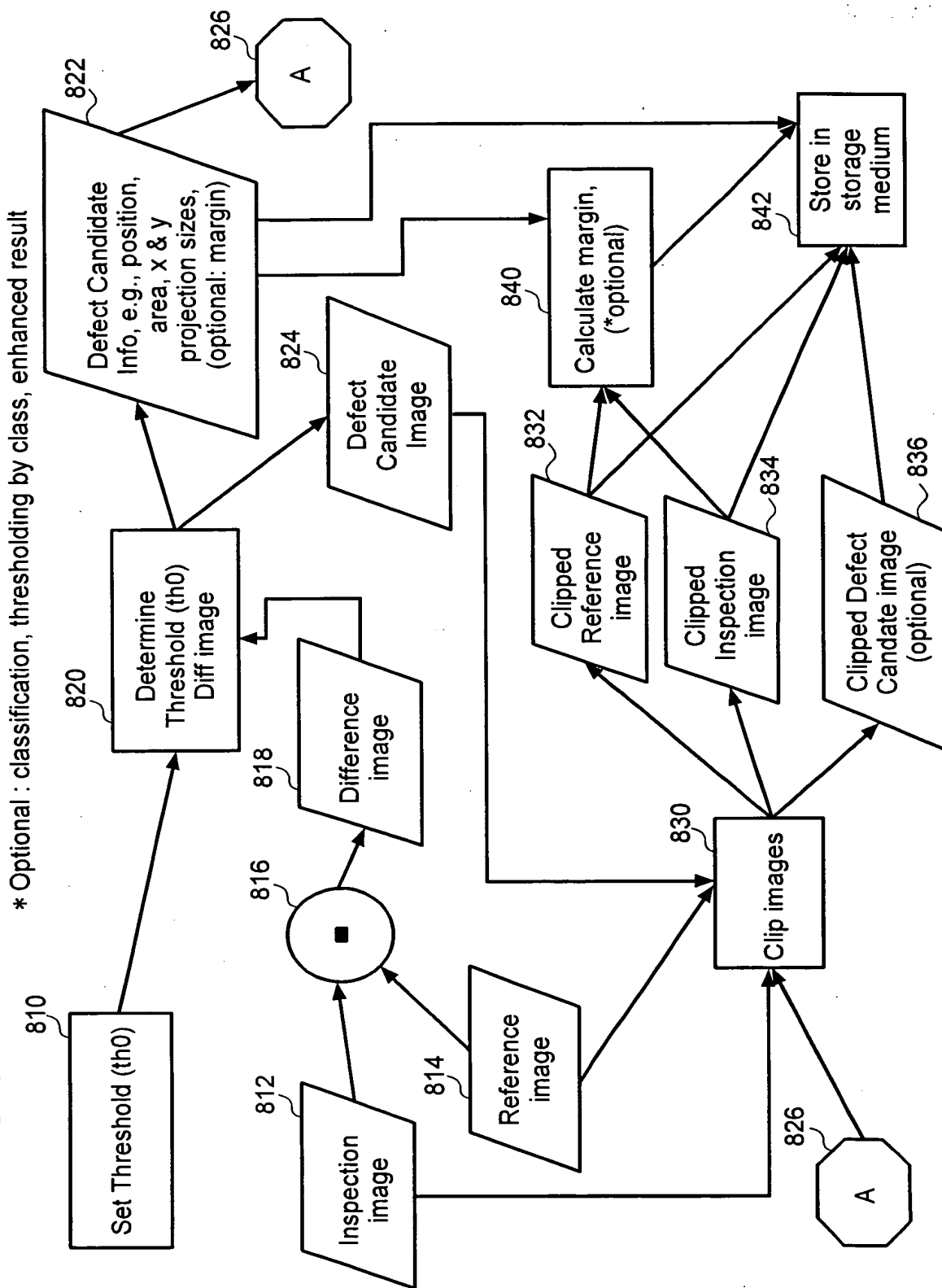
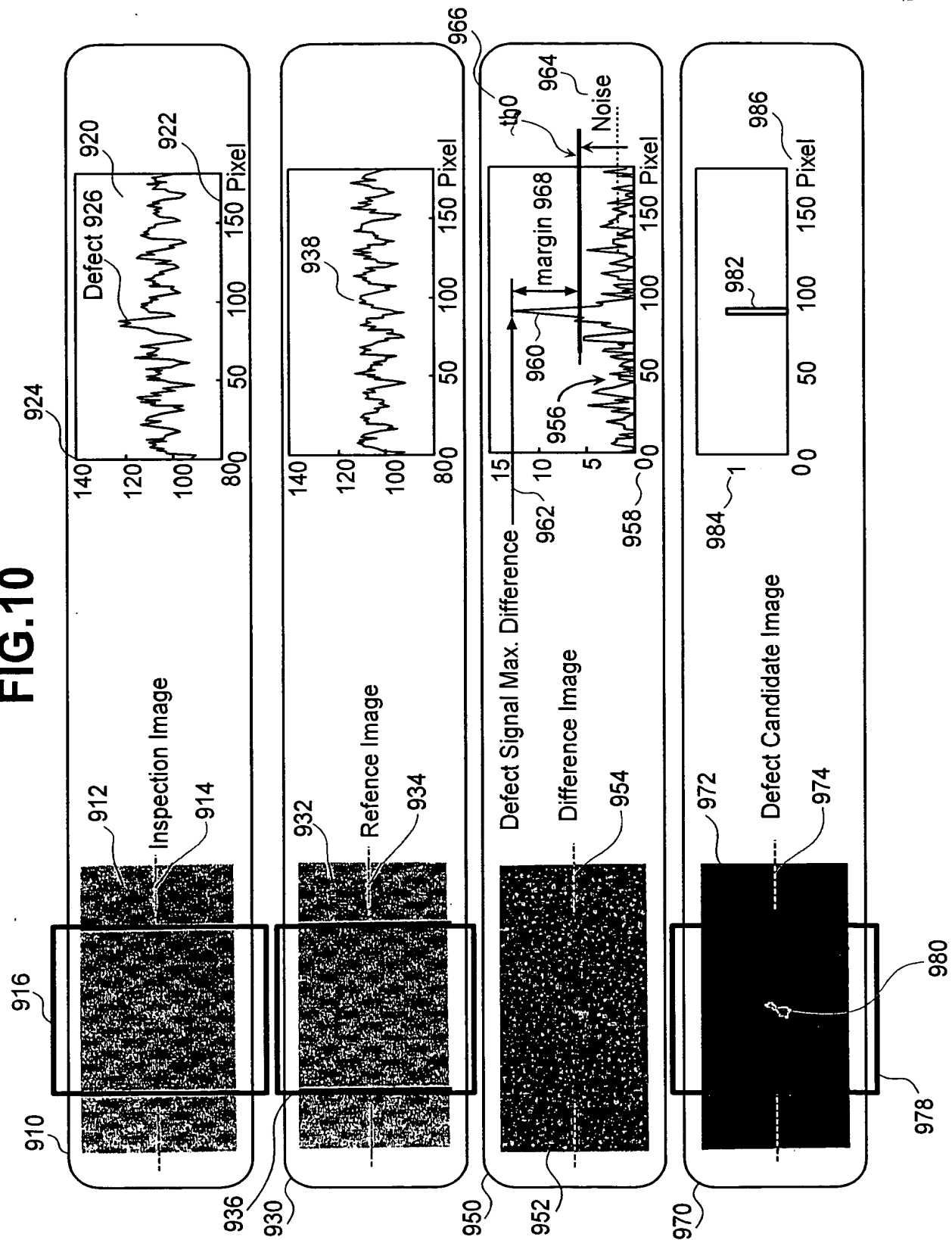
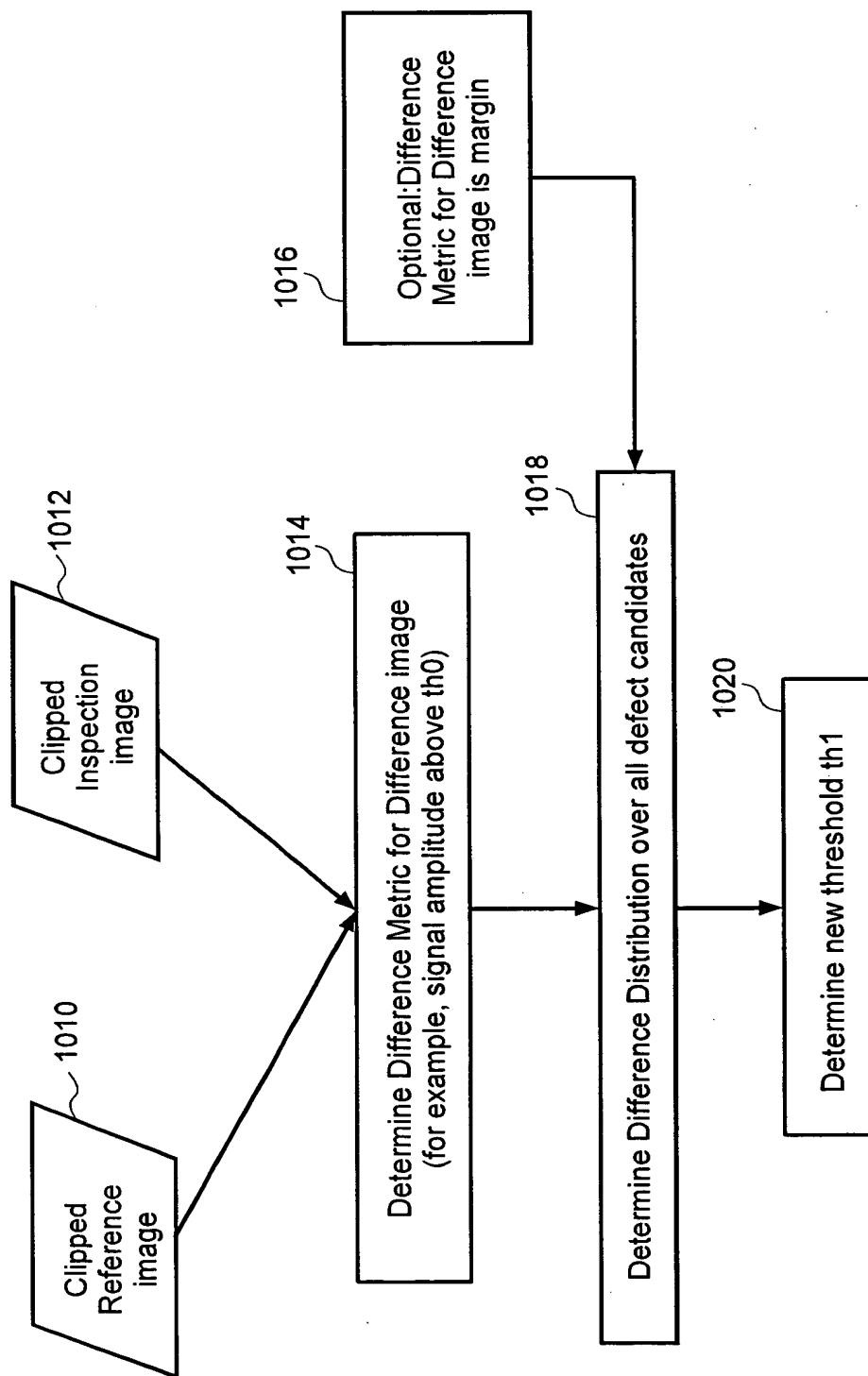


FIG.10



APPROVED	O.G. FIG.	
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FIG. 11



APPROVED	O.G. FIG.	
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**FIG.12**

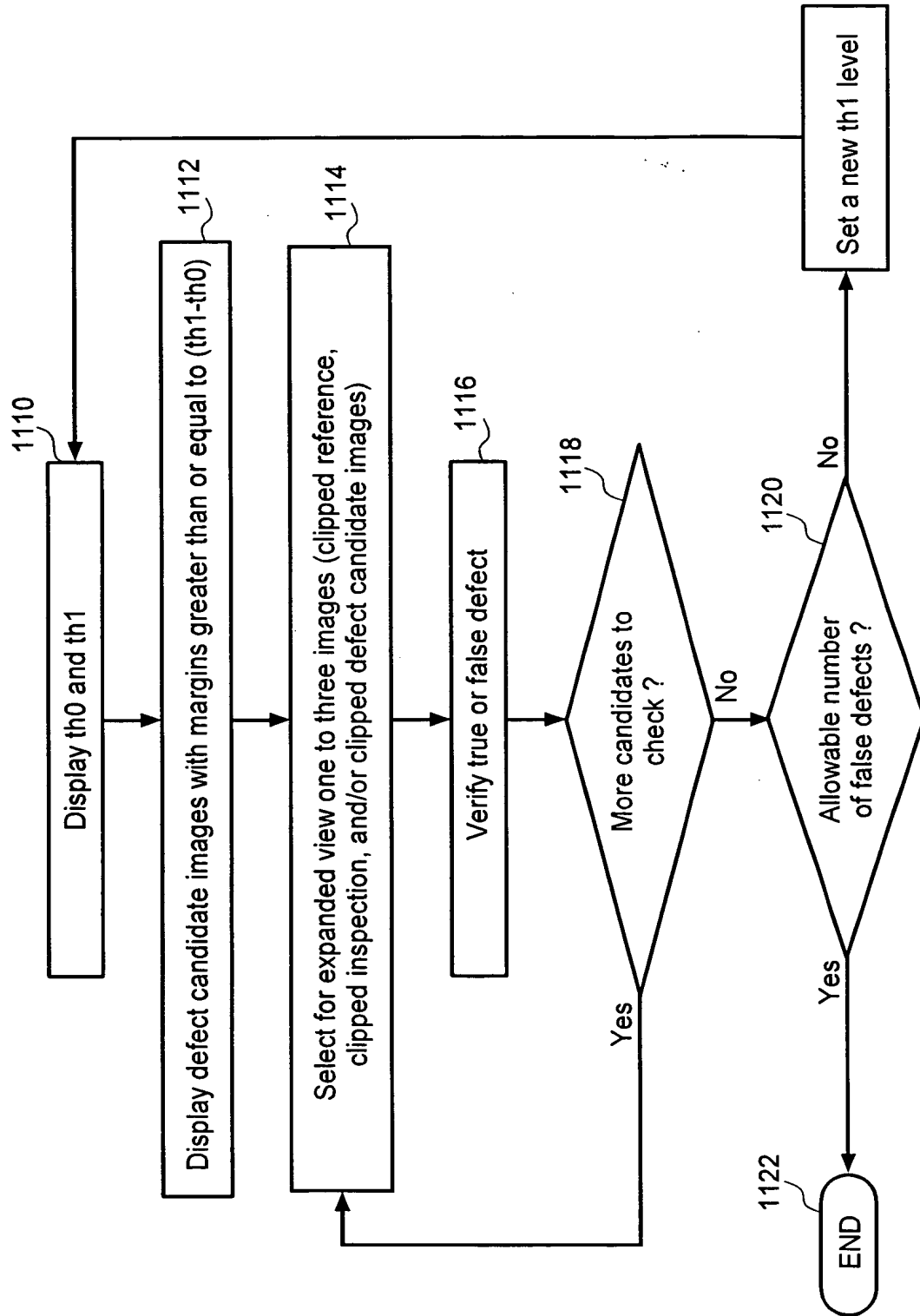
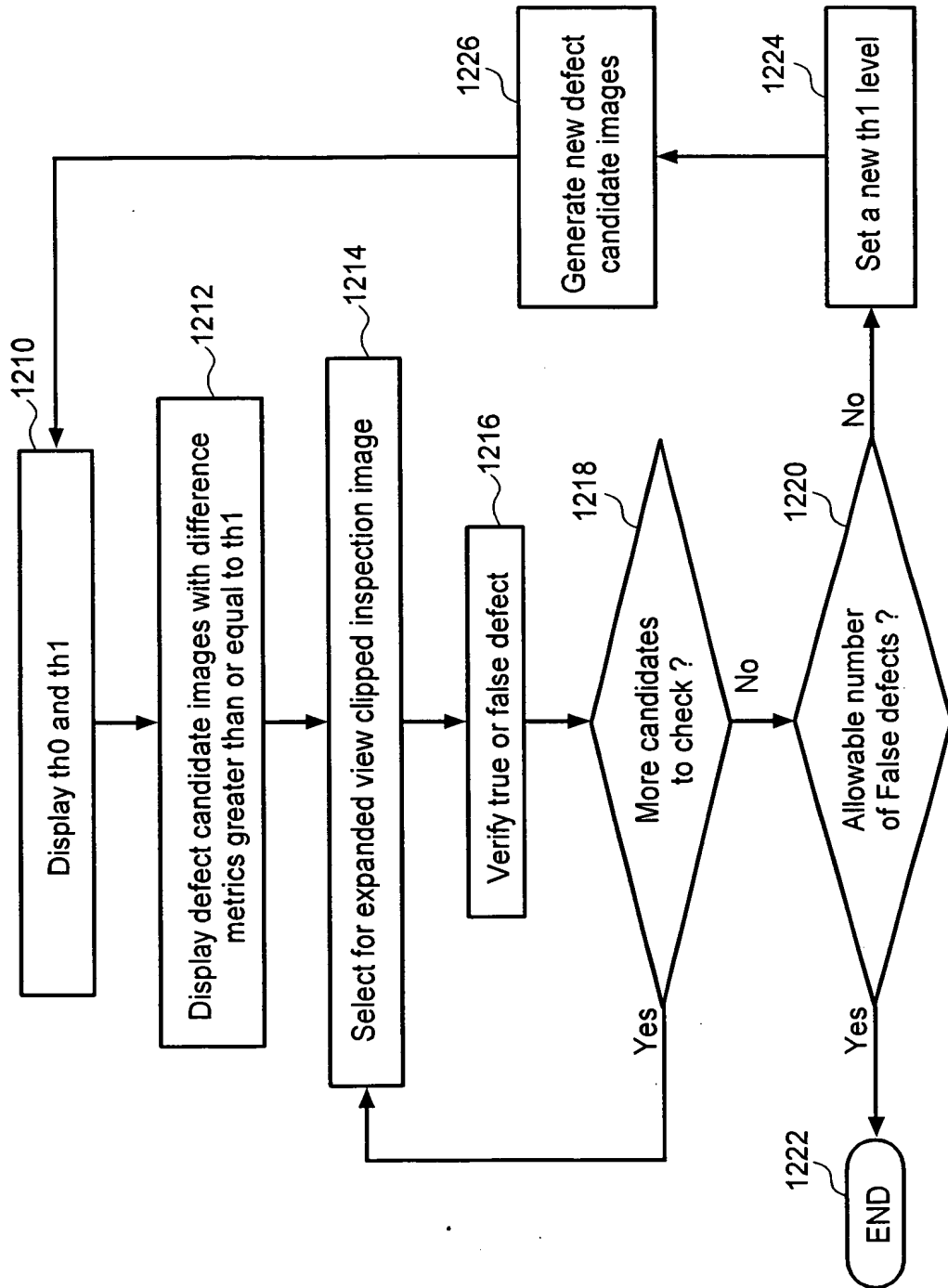


FIG.13



**FIG. 14**

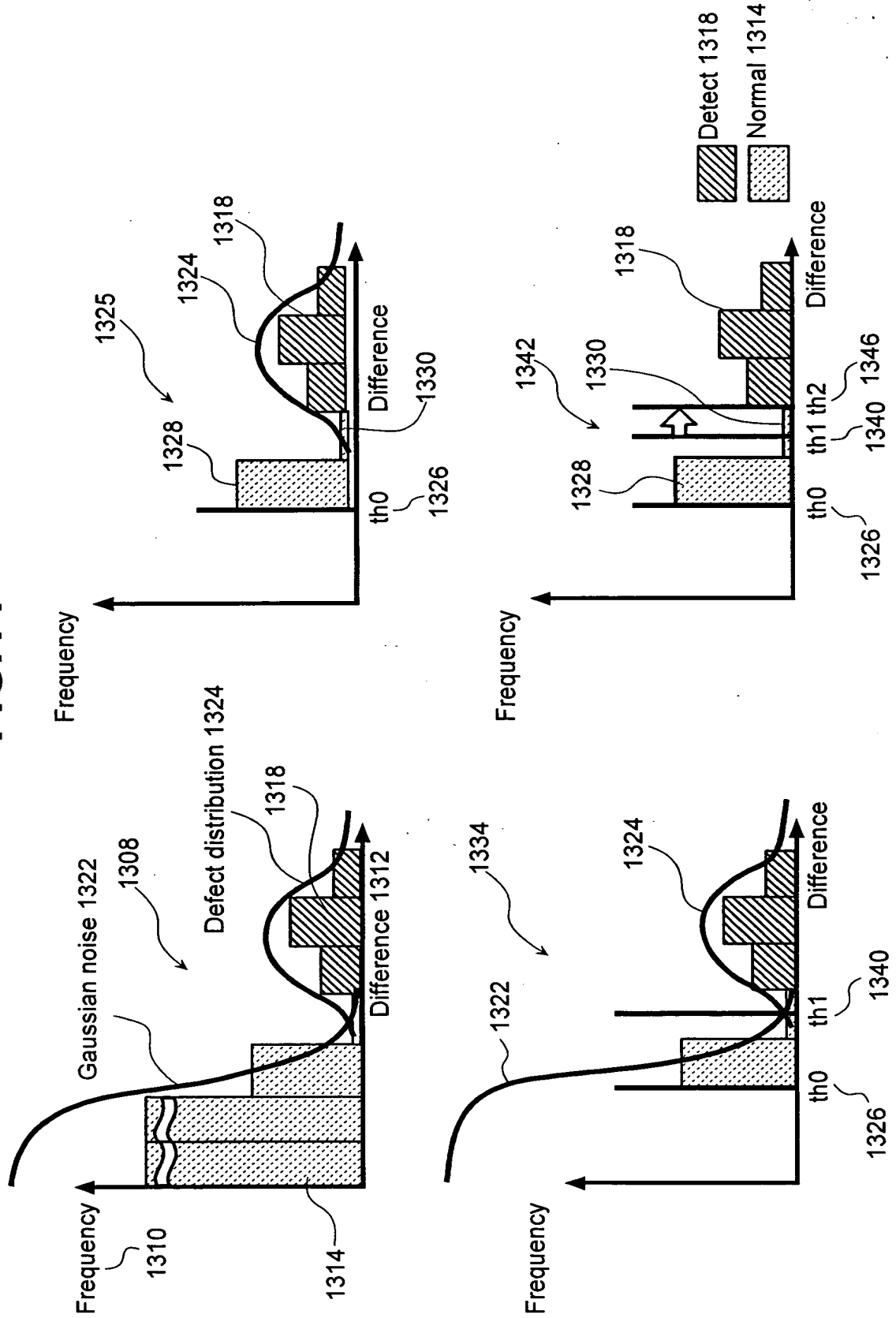


FIG.15

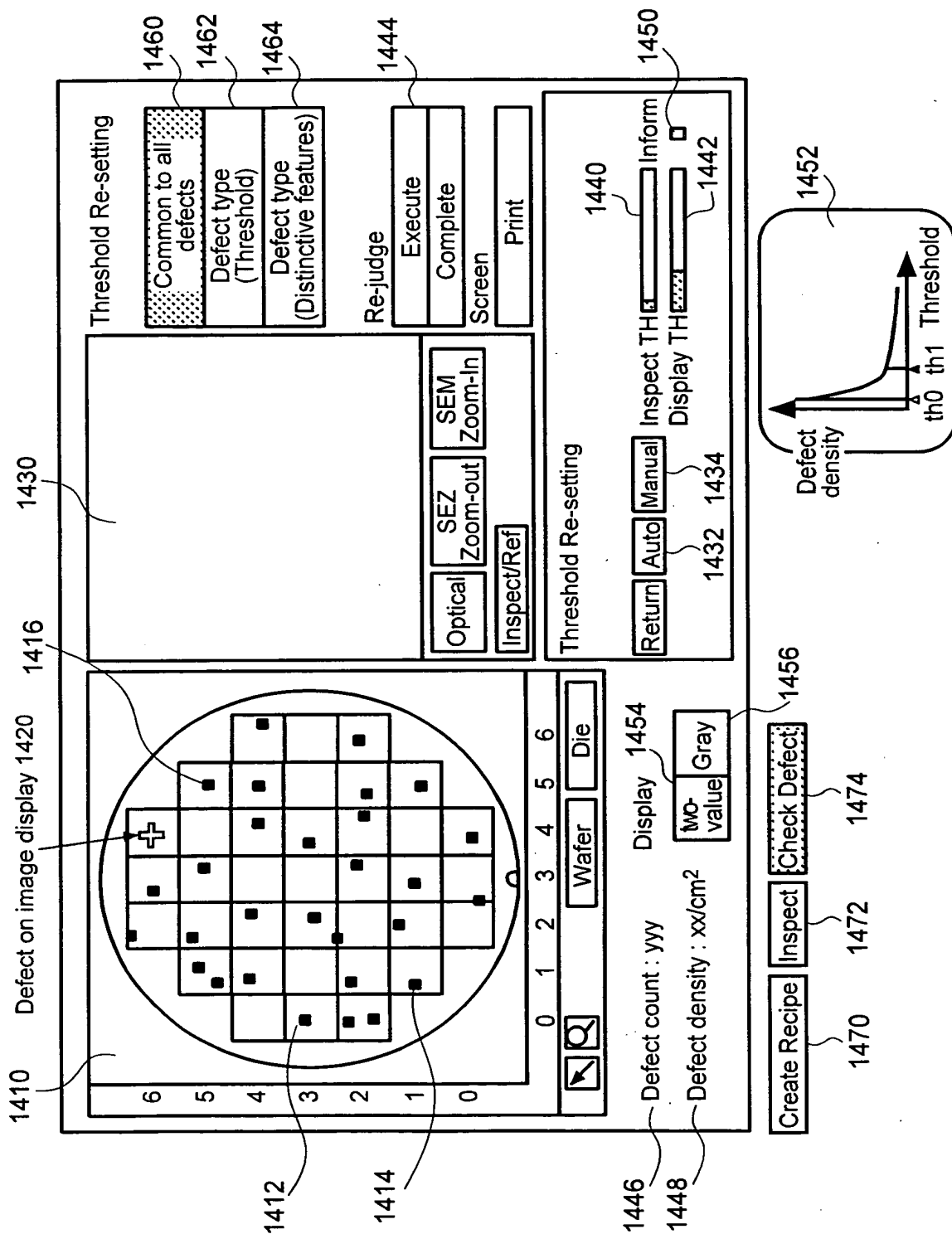
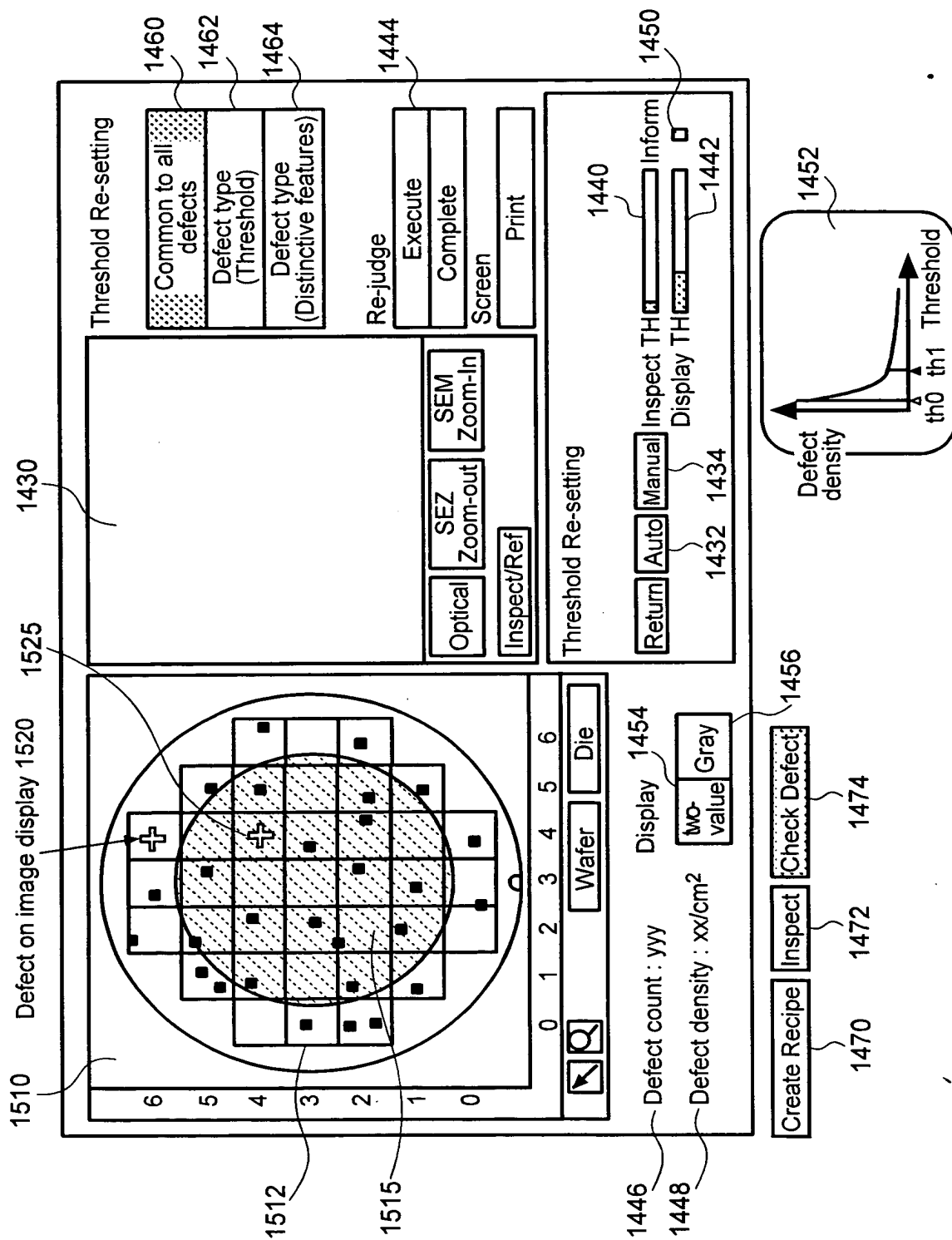




FIG.16



**FIG.17**

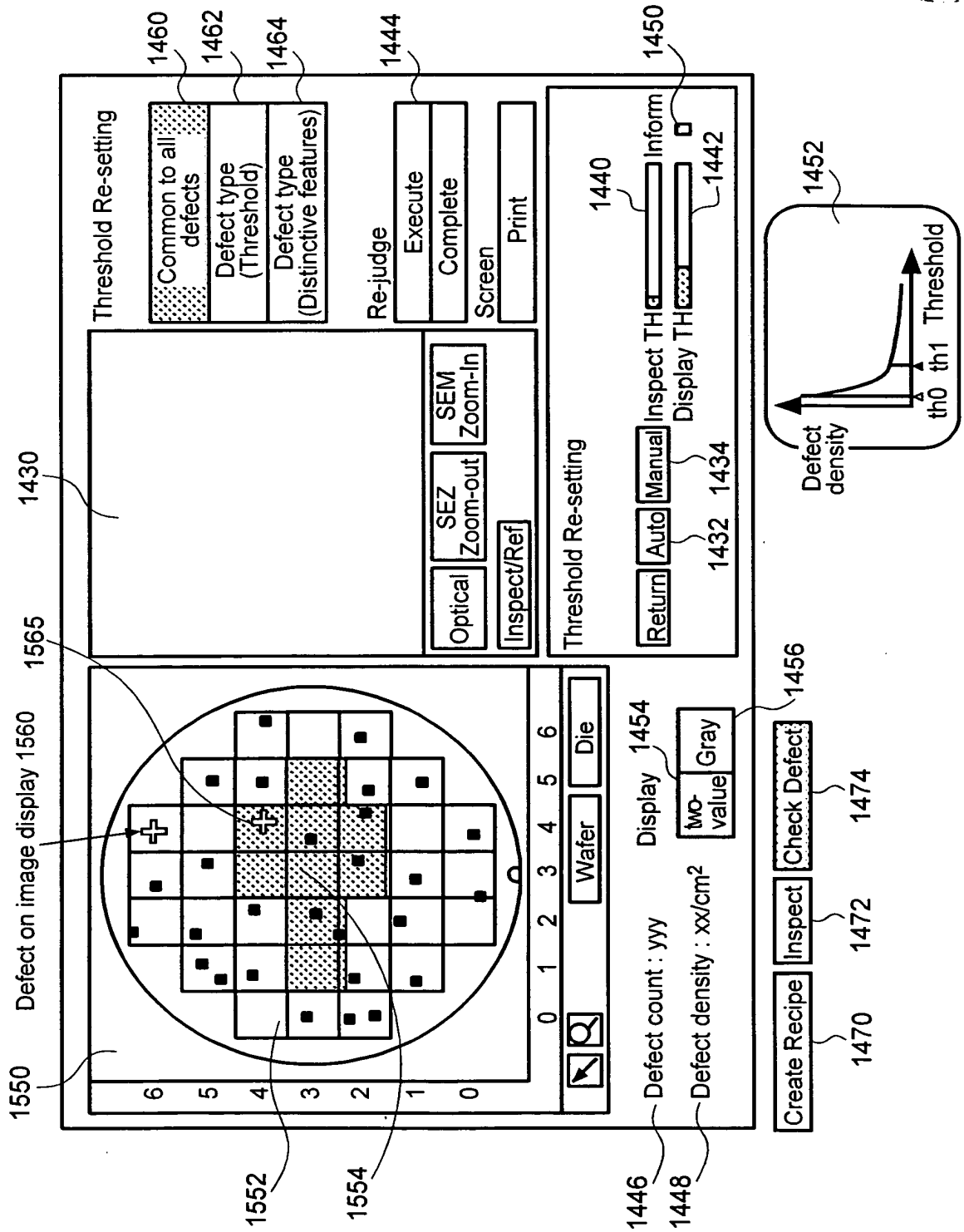
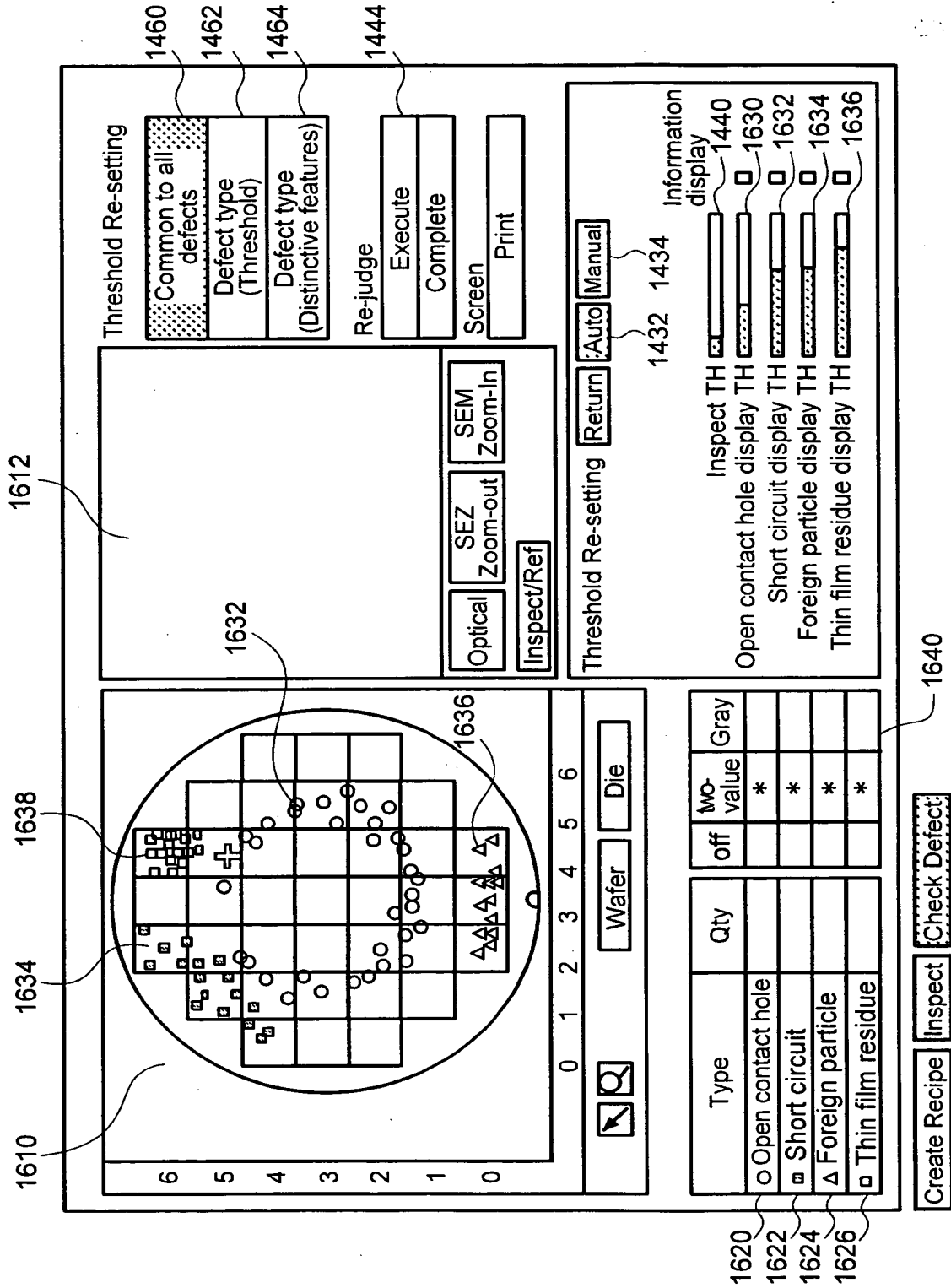
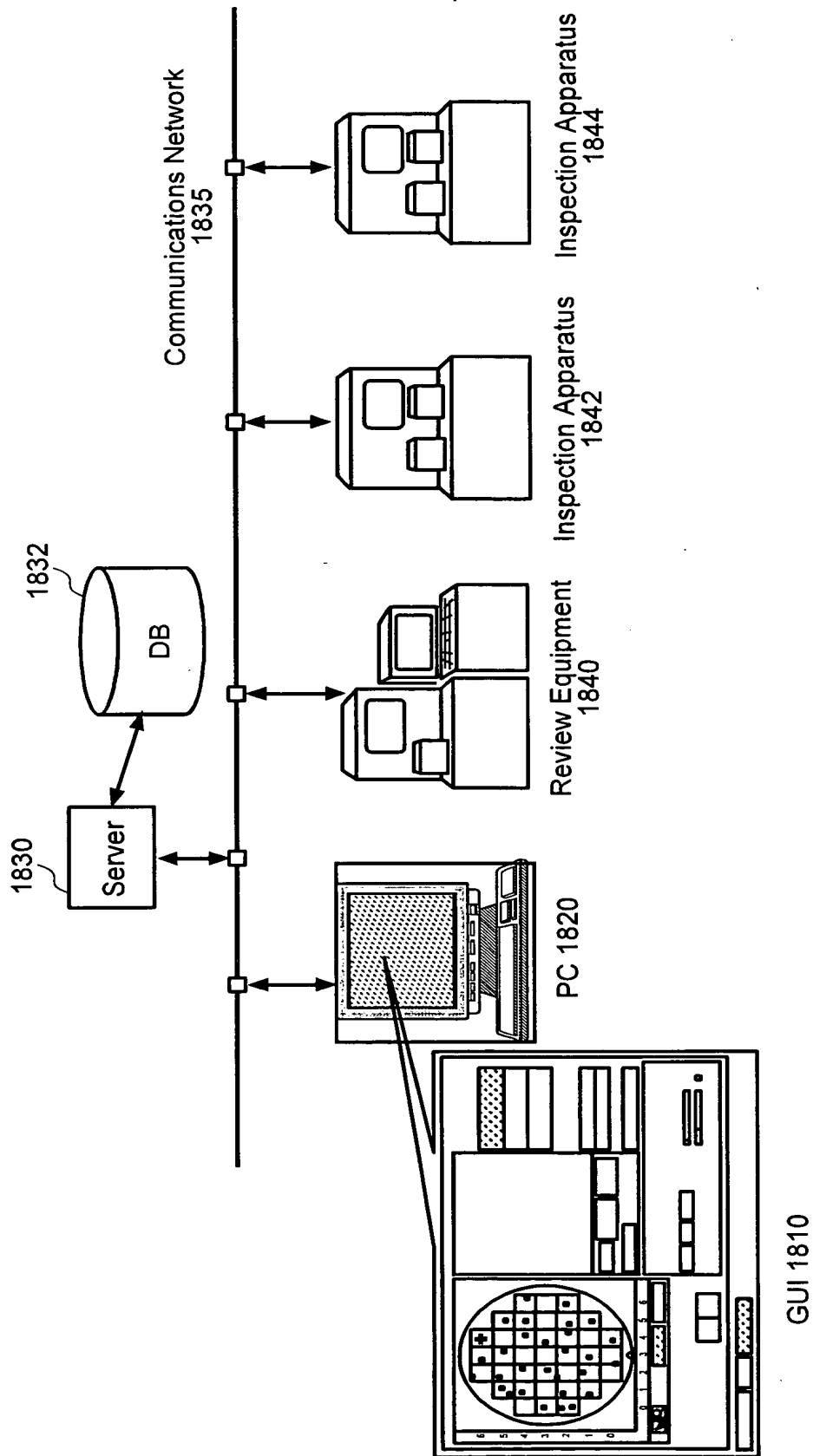


FIG. 18



**FIG.19**



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FIG. 20

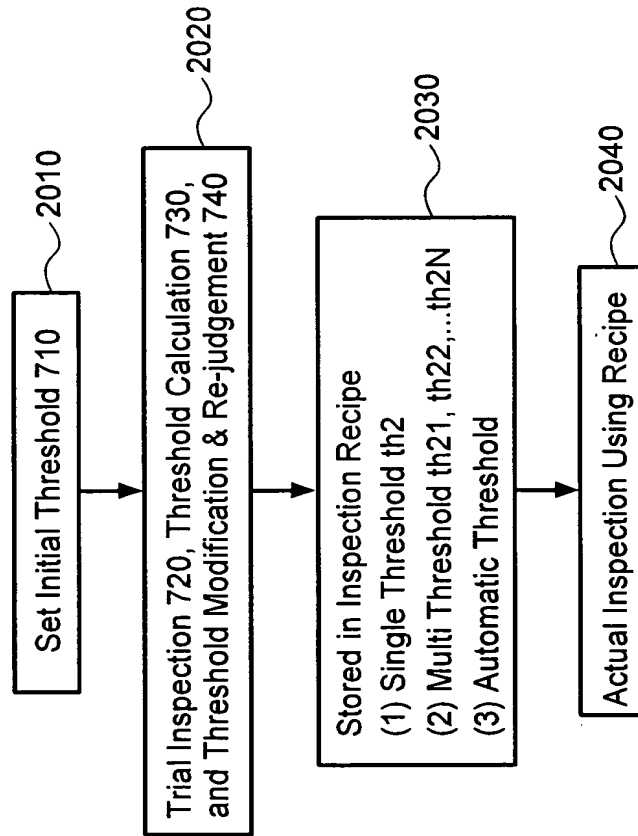


FIG.21

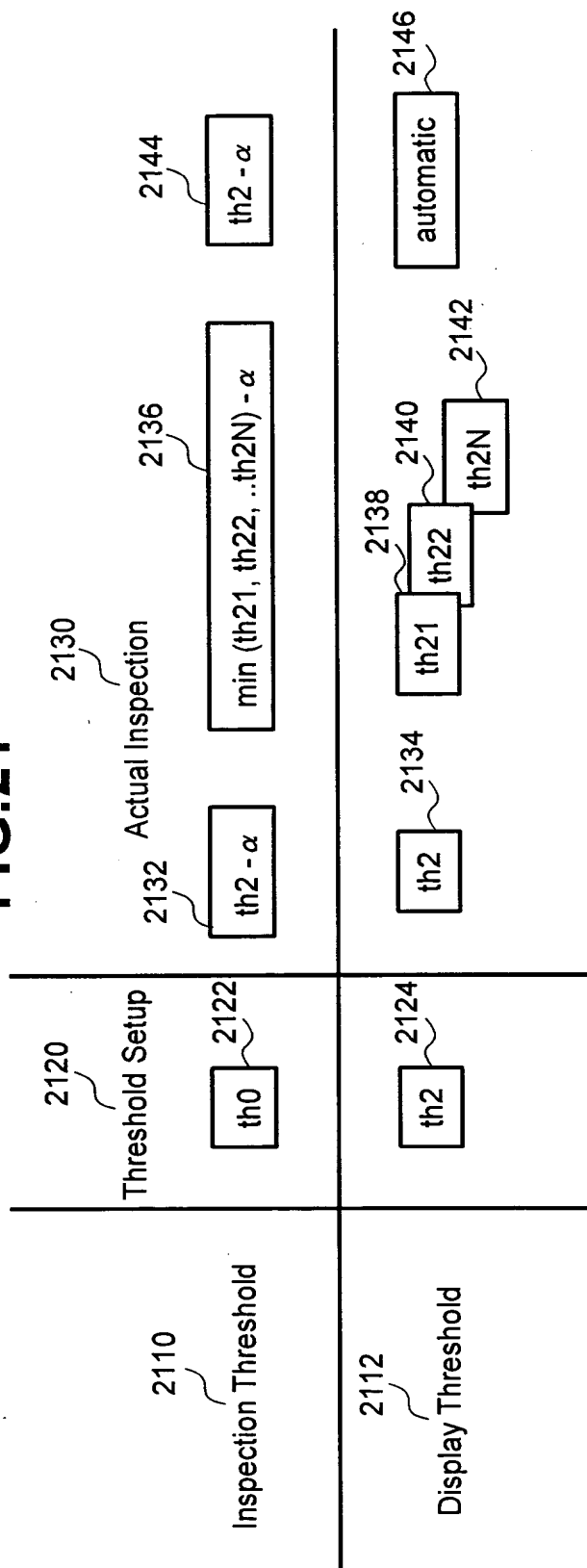


FIG.22

